



<b>Name:</b>	<b>Sukanya S Meher</b>
<b>Affiliation:</b>	HYPRES Inc
<b>Position:</b>	Member of Technical Staff
<b>Previous Positions:</b>	Graduate Research Assistant, Columbia University, 2015-2016 Teaching Assistant for Emerging Nanoelectronics Devices, 2016 Intern, Expert HDL, 2014-2015 SOC Verification Intern, NXP Semiconductors, 2014
<b>Education:</b>	M.S., Electrical Engineering, Columbia University, NY, 2017 B.E., Electronics and Communication, Manipal Institute of Technology, India, 2014
<b>Research Interests/Areas of Expertise:</b>	Superconductor Circuits in RSFQ/ERSFQ logic, SFQ Cell Library Development, Electronic Design Automation for Superconductor Electronics, Device Modeling and Simulation
<b>Publications:</b>	More than 15 papers in various journals including IEEE Transactions on Applied Superconductivity (TAS), IOP Journal of Physics
<b>Approximate Number of Years in Applied Superconductivity:</b>	7 years
<b>Membership in Professional Societies:</b>	Senior Member, IEEE   Member, ACM
<b>Previous ASC Service:</b>	Participant of ASC 2018, 2020, 2022 Electronics Program Committee ASC 2024 Session Chair/Moderator ASC 2020 Reviewer for IEEE TAS Special Issue on ASC 2022, EUCAS 2023
<b>Service to Related Conferences:</b>	Co-Chair, IEEE Young Professionals Council on Superconductivity (CSC) Communications Lead, IEEE Young Professionals Climate and Sustainability Taskforce Program Committee, International Workshop on Quantum, Cryogenic and Superconductive Computing (QUEST 2024) Expert Reviewer for various journals such as IEEE TAS, TCAD, TCAS, JEDS, Scientific Reports
<b>Honors and Awards:</b>	2023 Women in Cryogenics and Superconductivity by Cryogenic Society of America Best paper award at National Technical Festival of Manipal Institute of Technology, 2013